Attorney Docket No. 00531/0001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants   :

Application No. :

Filed   :

Title   :

Group Art Unit   :

Examiner   :

> The following Request is being made for a group of patent applications.
>
> The required identification information for each of the patent applications is provided on an attached spreadsheet.

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Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR WITHDRAWAL AS ATTORNEY OR AGENT

On behalf of myself and all the attorneys/agents of Schulte Roth & Zabel LLP

(Customer Number 29619) I hereby apply to withdraw as attorney of record for the patent

applications identified on Appendix A (attached hereto).

This Request is being made in response to instructions communicated by the

Assignee of record for the aforementioned patent applications, ATI Technologies, Inc. The *

Assignee requests that all their patent application files currently being prosecuted by Schulte

Roth & Zabel LLP be transferred to the individual and the law firm identified below.

Change the correspondence address and direct all future correspondence to:

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Triplicate copies of the Request are enclosed.

Respectfully submitted,

Schulte Roth & Zabel LLP
Attorneys for Applicants
919 Third Avenue
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212-756-2000

By: _____
Joel E. Lutzker
Reg. No. 29,406 ·

Dated: August 29, 2003
New York, New York

Express Mail mailing label No. EV 320045596 US
Date of Deposit: August 29, 2003

I hereby certify that this paper or fee is being deposited with the
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addressed to the Commissioner for Patents, PO Box 1450, Alexandria,
VA 22313-1450.

Signature: _____
David Toma

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APPENDIX A

Attorney Docket No.	Application No. (Patent No.)	Applicant(s)	Filed	Title	Group Art Unit	Examiner
005231/0003	09/385,394	John S. Yates Jr., et al.	08/30/1999	COMPUTER FOR EXECUTING TWO DIFFERENT INSTRUCTION SETS	2183	Richard Ellis
005231/0004	09/348,317	John S. Yates Jr., et al.	07/07/1999	RECORDING CLASSIFICATION OF INSTRUCTIONS EXECUTED BY A COMPUTER	2784	Not Yet Assigned
005231/0005	09/239,194	Sandeep Nijhawan, et al.	01/28/1999	EXECUTING PROGRAMS FOR A FIRST COMPUTER ARCHITECTURE ON A COMPUTER OF A SECOND ARCHITECTURE	2127	Kenneth Tang
005231/0006	09/322,443	John S. Yates Jr., et al.	05/28/1999	PROFILING OF COMPUTER PROGRAMS EXECUTING IN VIRTUAL MEMORY SYSTEMS	2122	Chameli Das
005231/0007	09/330,852	John S. Yates Jr., et al.	06/11/1999	PROFILING RANGES OF EXECUTION OF A COMPUTER PROGRAM	2763	Not Yet Assigned
005231/0008	09/332,263	John S. Yates Jr., et al.	06/11/1999	PROFILING PROGRAM EXECUTION BY DENSE TRACE PROFILING AND STATISTICAL PROFILING	2127	Kenneth Tang
005231/0009	09/425,401	John S. Yates Jr., et al.	10/22/1999	PROFILING PROGRAM EXECUTION TO IDENTIFY FREQUENTLY EXECUTED PORTIONS AND ASSIST BINARY TRANSLATION	2151	Not Yet Assigned
005231/0010	09/334,530	John S. Yates Jr., et al.	06/16/1999	PROFILING EXECUTION OF COMPUTER PROGRAMS	2122	Chameli Das

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APPENDIX A

Attorney Docket No.	Application No. (Patent No.)	Applicant(s)	Filed	Title	Group Art Unit	Examiner
005231/0011	09/339,749	John S. Yates Jr., et al.	06/24/1999	PROFILING PROGRAM EXECUTION INTO REGISTERS OF A COMPUTER	2122	Wei Y Zhen
005231/0012	09/339,797	John S. Yates Jr., et al.	06/24/1999	MODIFYING PROGRAM EXECUTION BASED ON PROFILING	2151	Not Yet Assigned
005231/0013	09/427,168	John S. Yates Jr., et al.	10/26/1999	TRANSFERRING EXECUTION FROM ONE COMPUTER INSTRUCTION STREAM TO ANOTHER	2183	Not Yet Assigned
005231/0014	09/426,989	John S. Yates Jr., et al.	10/26/1999	TABLE LOOK-UP FOR CONTROL OF INSTRUCTION EXECUTION	2183	Not Yet Assigned
005231/0015	09/429,377	John S. Yates Jr.	10/28/1999	IMPROVING COMPUTER EXECUTION BY OPPORTUNISTIC ADAPTATION	2758	Not Yet Assigned
005231/0016	09/429,094	John S. Yates Jr., et al.	10/28/1999	SIDE TABLES ANNOTATING AN INSTRUCTION STREAM	2155	David Eng
005231/0017	09/428,850 (6,397,379)	John S. Yates Jr., et al.	10/28/1999	RECORDING I/O MEMORY REFERENCES IN PROGRAM EXECUTION PROFILE	2122	T. Ingberg
005231/0018	09/434,198 (6,549,959)	John S. Yates Jr., et al.	11/04/1999	DETECTING MODIFICATION TO COMPUTER MEMORY BY A DMA DEVICE	2182	Rijue Mai
005231/0019	09/432,752	John S. Yates Jr., et al.	11/03/1999	DETECTING INVALIDATION OF TRANSLATED OBJECT CODE WHEN SOURCE CODE IS MODIFIED (XP bit)	2751	Not Yet Assigned

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APPENDIX A

Attorney Docket No.	Application No. (Patent No.)	Applicant(s)	Filed	Title	Group Art Unit	Examiner
005231/0020	09/434,394	John S. Yates Jr., et al.	11/04/1999	DETECTING REORDERED SIDE-EFFECTS	2123	Not Yet Assigned
005231/0021	09/432,753	John S. Yates Jr., et al.	11/03/1999	SAFETY-NET PARADIGM FOR MANAGING TWO EXECUTION MODES	2183	Not Yet Assigned
005231/0022	09/102,028 (6,563,863)	Brook S. Read	06/22/1998	COMPUTER MODEM	2631	Jean Corrielus
005231/0023	09/323,983 (6,577,639)	Brook S. Read	06/01/1999	REDUCING MODEM TRANSMIT LATENCY	2631	Kenneth N. Vanderpaye
005231/0024	60/176,610	Korbin S. Van Dyke	01/18/2000	HARDWARE INVENTIONS RELATED TO CONVERTER	N/A	N/A
005231/0026	09/666,110	Korbin S. Van Dyke, et al.	09/20/2000	COMPUTER FOR EXECUTION OF TWO INSTRUCTION SETS	2183	Not Yet Assigned
005231/0027	09/667,226	Korbin S. Van Dyke, et al.	09/21/2000	EXCEPTION MECHANISM OF A COMPUTER	2873	Not Yet Assigned
005231/0028	09/626,325	Sandeep Nijhawan, et al.	07/26/2000	COMPUTER WITH TWO OPERATING SYSTEMS	2183	Not Yet Assigned
005231/0029	09/672,440	Harry G. Manson, et al.	09/28/2000	MANAGING INSTRUCTION SIDE-EFFECTS	2183	Not Yet Assigned
005231/0030	09/672,841	Korbin S. Van Dyke, et al.	09/28/2000	VALIDATION OF MEMORY REFERENCES	2183	Not Yet Assigned
005231/0031	09/627,424	Korbin S. Van Dyke, et al.	09/28/2000	COMPLEX INSTRUCTION SET COMPUTER	2183	Not Yet Assigned

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